



FIG. 1

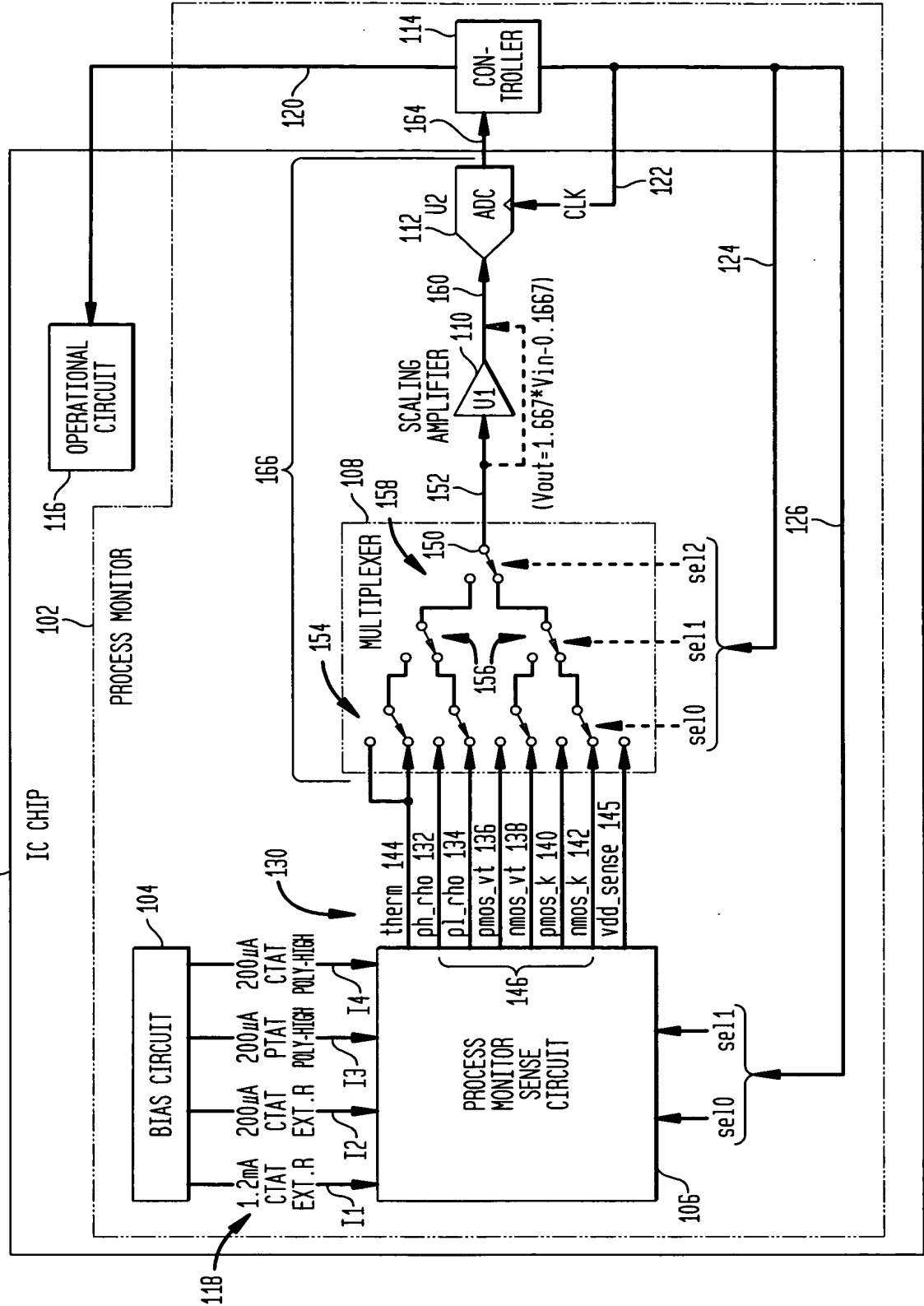


FIG. 1A

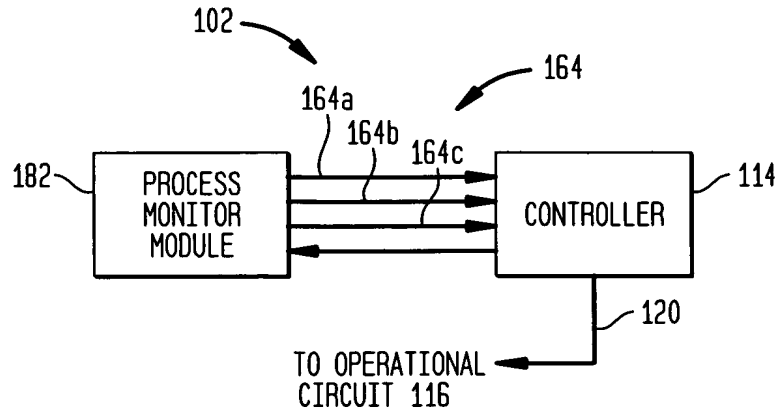
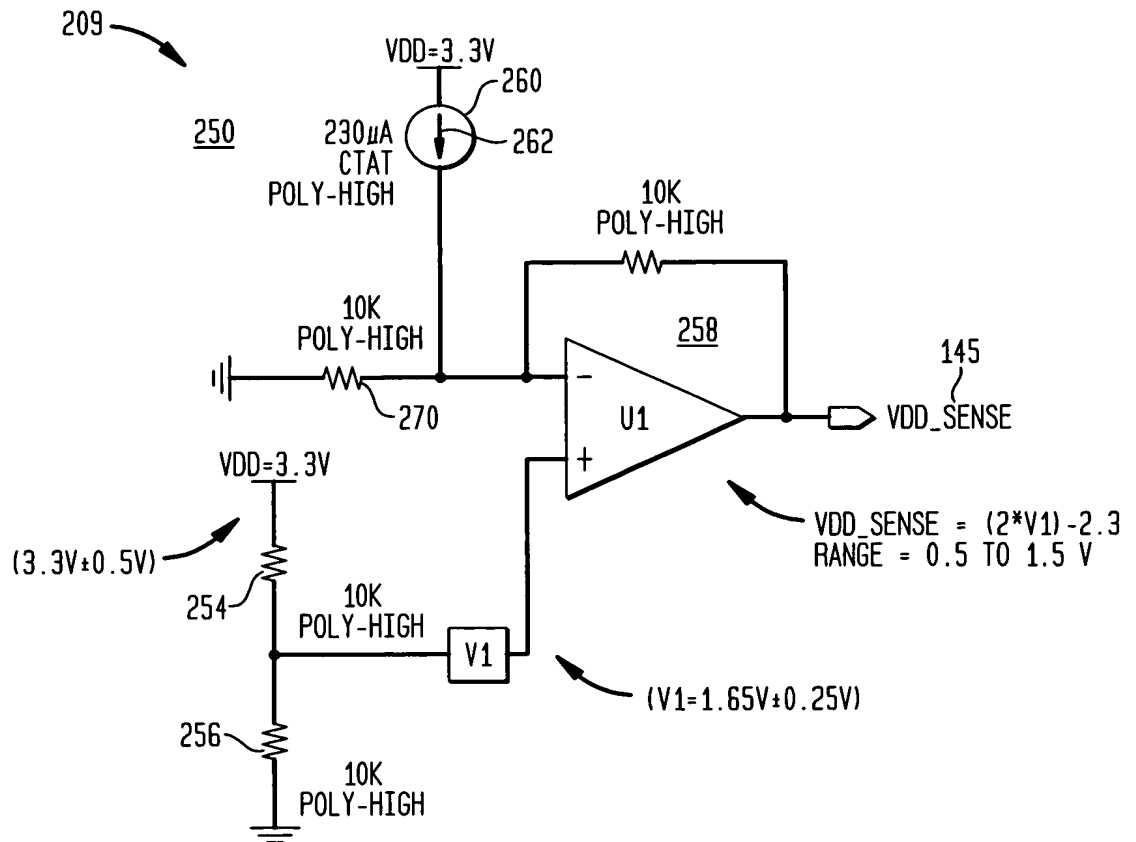
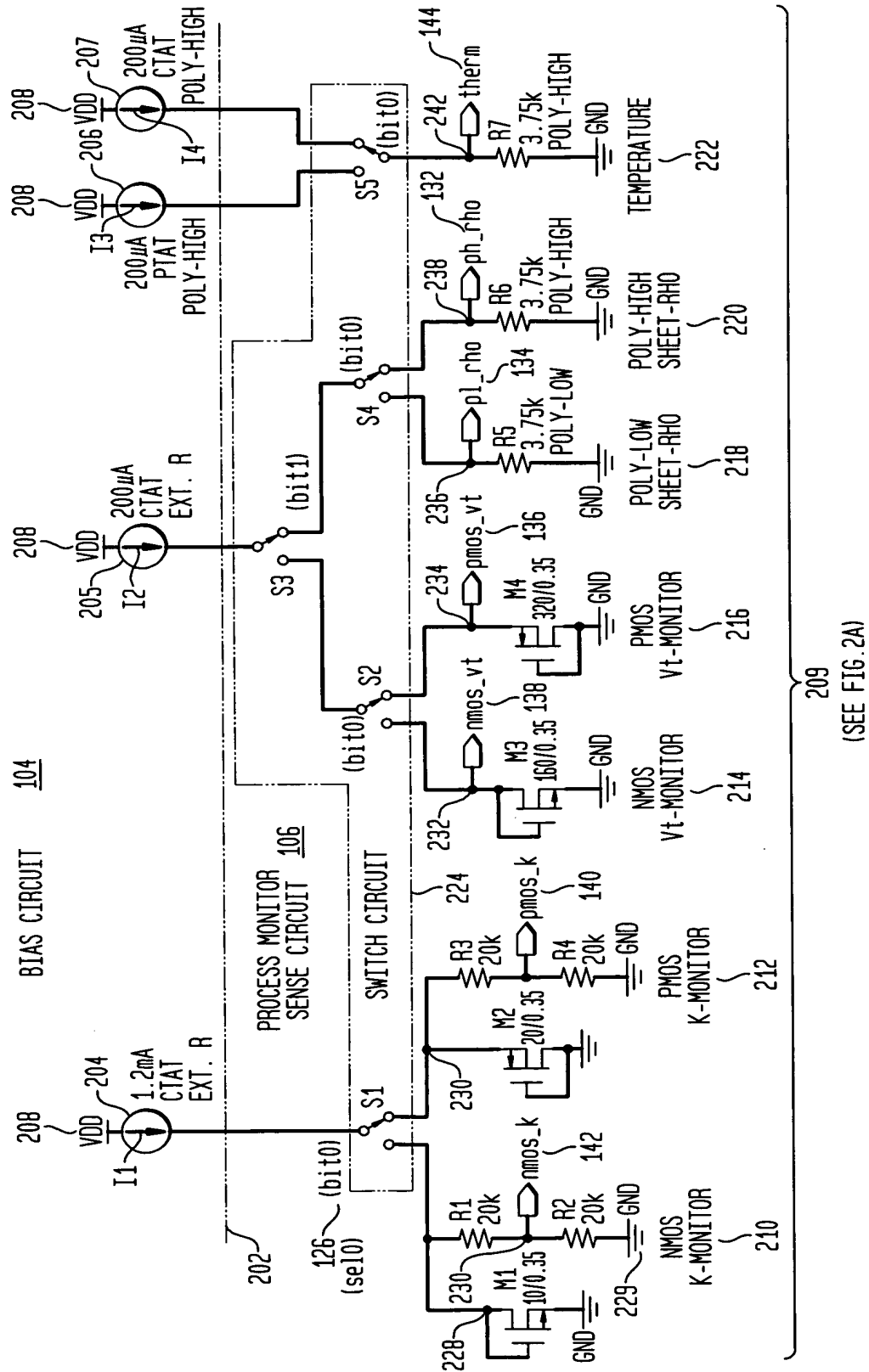


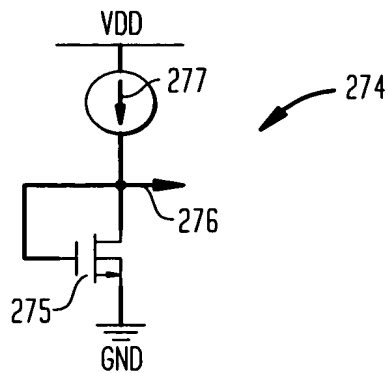
FIG. 2A



**FIG. 2**



**FIG. 2B**



**FIG. 3**

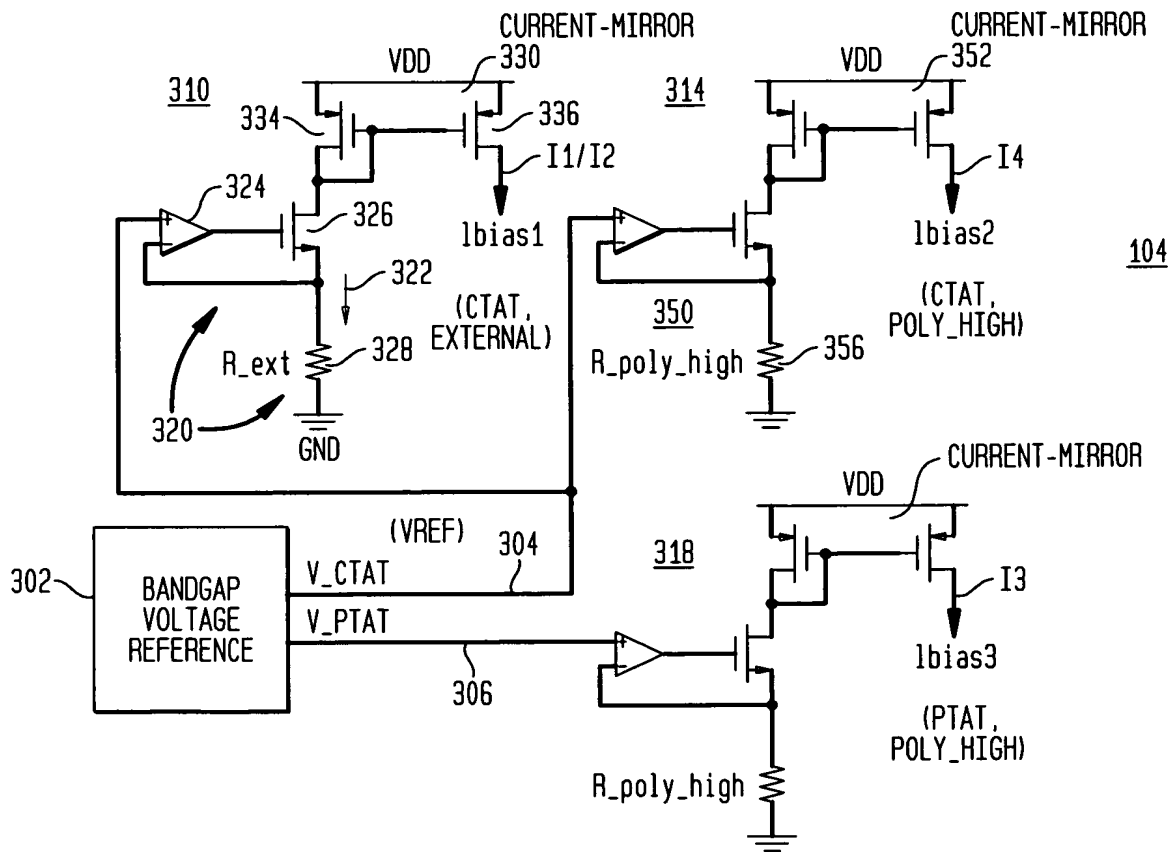


FIG. 4

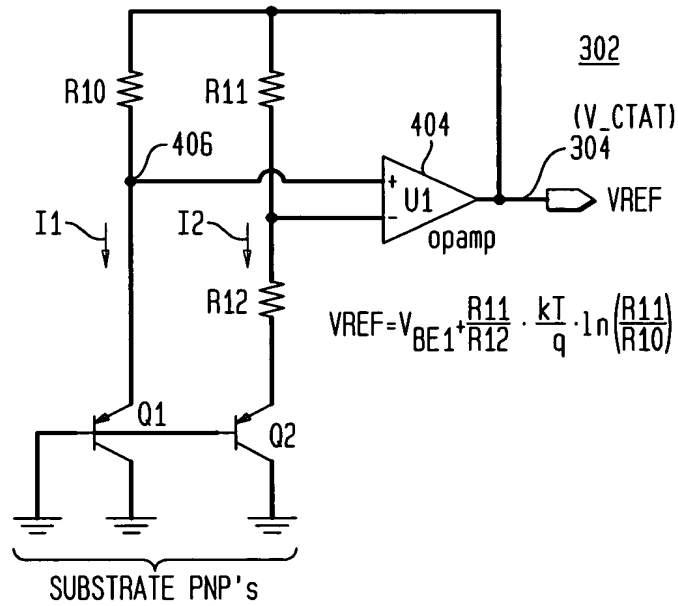
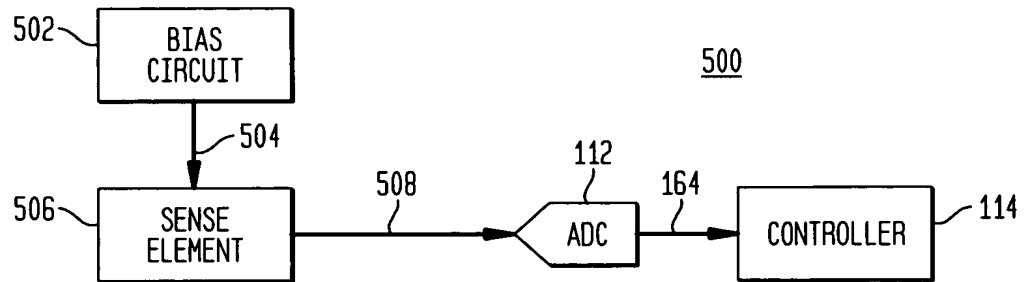


FIG. 5



**FIG. 6**  
 OPERATIONAL CIRCUIT

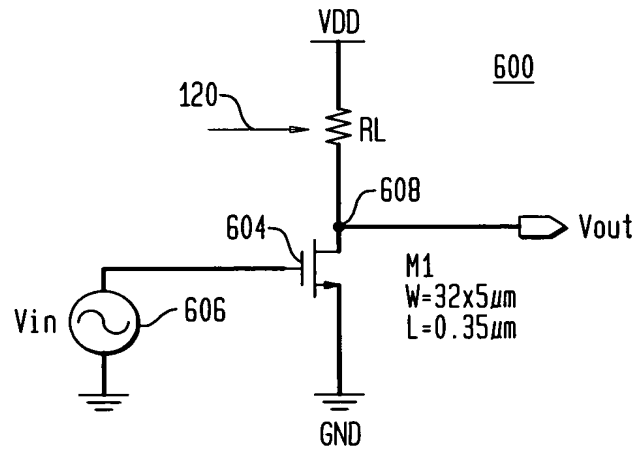
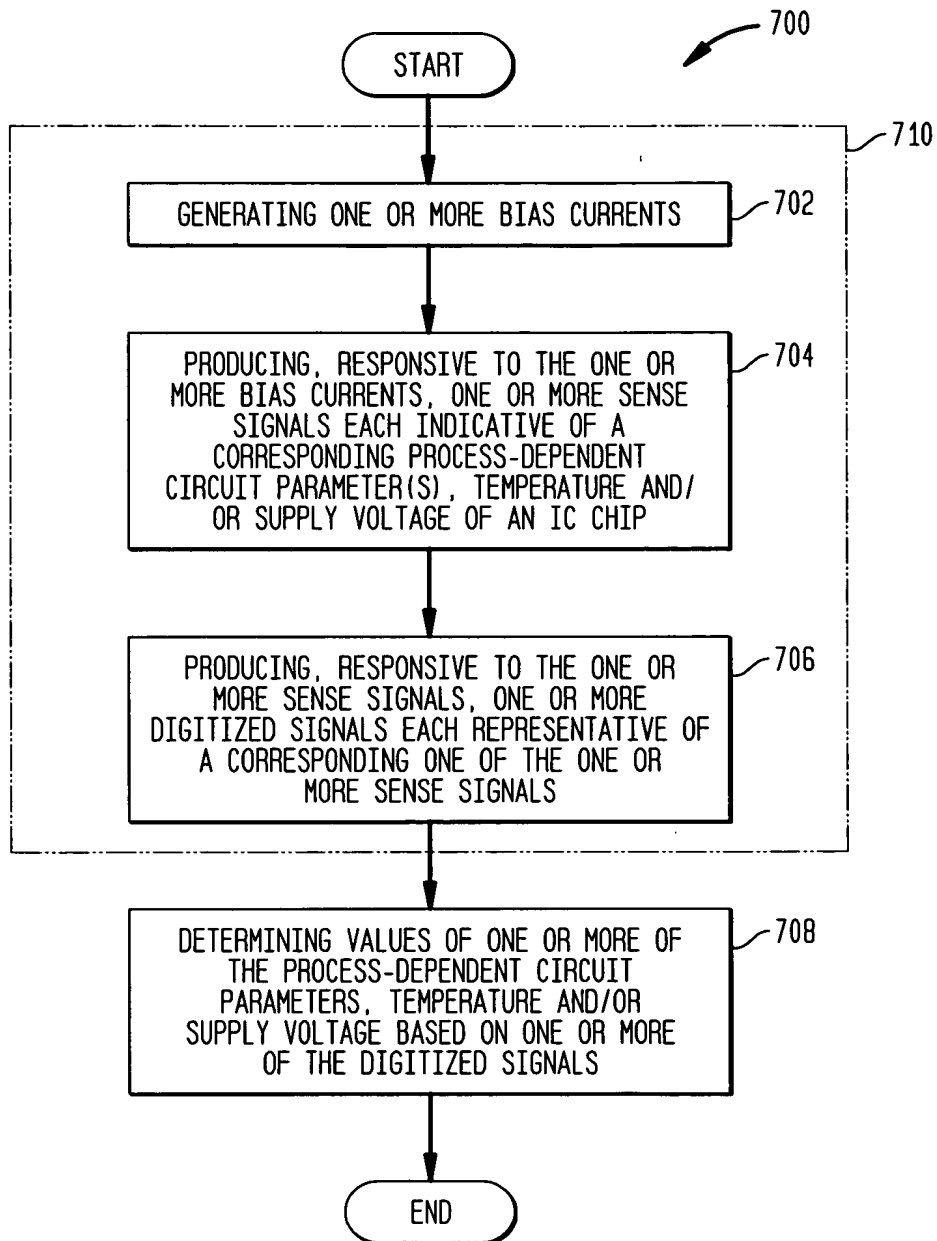
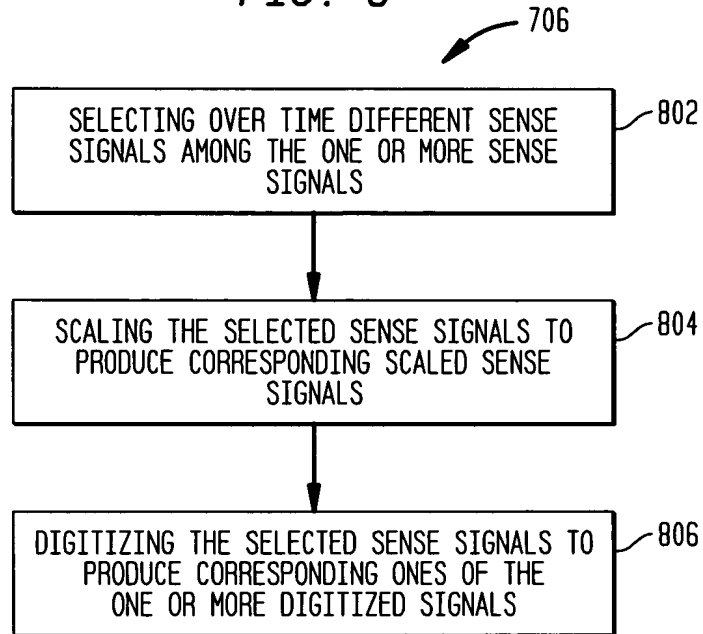


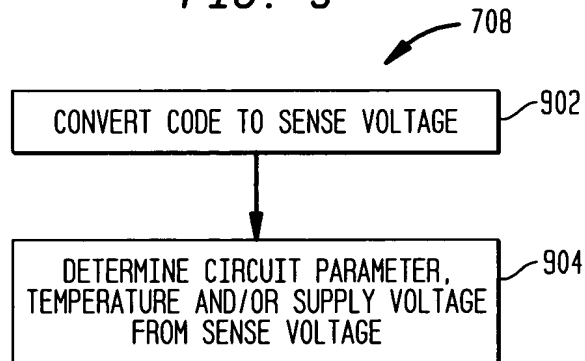
FIG. 7



**FIG. 8**



**FIG. 9**





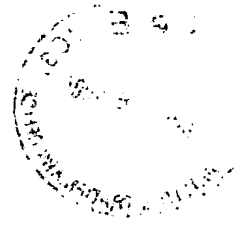


FIG. 10

